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PATENT
Docket No. 51889/3

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of)	
)	
Douglas R. Hackler, Sr., et al.)	
)	
Serial No.:)	Art Unit
10/719,119)	2814
Filed:)	
November 21, 2003)	
For:)	
DOUBLE-GATED TRANSISTOR CIRCUIT)	
Examiner:)	
Phat X. Cao)	
Patent No.:)	
7,019,342)	
Issue Date:)	
March 28, 2006)	

REQUEST FOR A CERTIFICATE OF CORRECTION
UNDER 37 C.F.R. § 1.323

Attn: Certificate of Correction Branch
Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Certificate
NOV 21 2006
of Correction

Sir:

In accordance with the provisions of 37 C.F.R. § 1.323 which implement 35 U.S.C. § 255, the Commissioner is respectfully requested to issue a Certificate of Correction to correct

11/17/2006 SSESHE1 00000092 7019342

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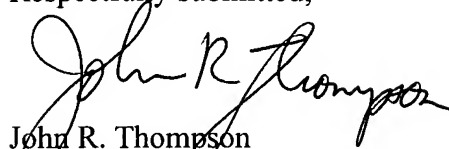
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mistakes in the above-identified patent. The enclosed Patent Office Form 1050 reflects the desired corrections.

Dated this 16 day of November, 2006.

Respectfully submitted,



John R. Thompson
Attorney for Patentee
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JRT:jbb
Docket: 51889/3

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,019,342 B2
DATED : March 28, 2006
INVENTOR(S): Douglas R. Hackler, Sr., et al.

It is certified that error appears in the above-identified patent and that said Letters Patent are hereby corrected as shown below:

Column 1, line 38, "... inputs values ..." change to -input values—

Column 1, line 39, "... output value of 0 ..." change to -outputs a value of 0—

Column 1, line 49, "... overall architecture" change to -overall architecture.—

Column 2, line 18, "... gates result ..." change to -gate result—

Column 2, line 21, "... bottom gates ..." change to -bottom gate—

Column 3, line 1-2, "... device of FIG. 13 device after ..." change to -device of FIG. 13 after—

Column 7, line 46, "... to FIG. 12 to, a method ..." change to -to FIG. 12, a method—

Column 7, line 60, "... the substrate layer 12 ..." change to -the SOI layer 16—

Column 10, line 21, "... numerous complex circuit ..." change to -numerous complex circuits—

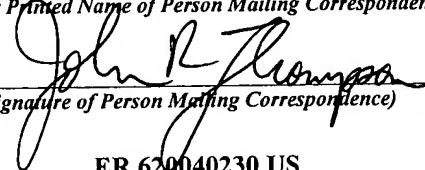
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➡ 1 of 1

This collection of information is required by 37 CFR 1.322, 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450
SaltLake-292924.1 0051889-00003

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CERTIFICATE OF MAILING BY "EXPRESS MAIL" (37 CFR 1.10)			Docket No. 51889/3	
Applicant(s): Douglas R. Hackler, Sr., et al.				
Application No. 10/719,119	Filing Date November 21, 2003	Examiner Pat X. Cao	Customer No. 32642	Group Art Unit 2814
Invention: DOUBLE-GATED TRANSISTOR CIRCUIT				
I hereby certify that the following correspondence: <div style="border: 1px solid black; height: 50px; margin: 10px 0;">See below:</div> <p style="text-align: center;"><i>(Identify type of correspondence)</i></p> <p>is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on</p> <p style="text-align: center;"><u>November 16, 2006</u> <i>(Date)</i></p> <div style="text-align: center; margin-top: 20px;"><u>John R. Thompson</u> <i>(Typed or Printed Name of Person Mailing Correspondence)</i>  <i>(Signature of Person Mailing Correspondence)</i> <u>ER 620040230 US</u> <i>("Express Mail" Mailing Label Number)</i></div>				
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